

Slater & Matsil, L.L.P.
 Suite 1000
 17950 Preston Road
 Dallas, Texas 75252-5793
 Phone: 972-732-1001 Facsimile: 972-732-9218

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To Commissioner for Patents	Total Pages Sent: 10
Mail Stop: Amendment	(including cover sheet)
Facsimile Number: (703) 872-9306	Transmission Date: January 31, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kowalski, *et al.* Docket No.: INF-125
 Serial No: 10/748,332 Art Unit: 2815
 Date Filed: December 31, 2003
 Title: Device Architecture and Process for Improved Vertical Memory Arrays

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- Amendment (9 pages)

Respectfully submitted,


 Julie Russell
 Legal Assistant

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Applicant: Kowalski, *et al.*

Docket No.: INF-125

Serial No.: 10/748,332

Art Unit: 2815

Filed: December 31, 2003

Examiner: Warren, Matthew E.

Title: Device Architecture and Process for Improved Vertical Memory Arrays

Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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AMENDMENT

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's Office Action mailed November 5, 2004. Please amend the above-referenced application as follows.

INF-125

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Amendment

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